

Memory Module Specification

KVR800D2N6/512
512MB 64M x 64-Bit DDR2-800
CL6 240-Pin DIMM

DESCRIPTION:

This document describes ValueRAM's 64M x 64-bit (512MB) DDR2-800 CL6 SDRAM (Synchronous **DRAM**) memory module. The components on this module include eight 64M x 8-bit DDR2-800 SDRAM in FBGA packages. This 240-pin DIMM uses gold contact fingers and requires +1.8V. The electrical and mechanical specifications are as follows:

FEATURES:

✓	Clock Cycle Time (tCK) CL=6	2.5ns (min.) / 8ns (max.)
✓	Row Cycle Time (tRC)	60ns (min.)
✓	Refresh to Active/Refresh Command Time (tRFC)	105ns
✓	Row Active Time (tRAS)	45ns (min.) / 70,000ns (max.)
✓	Single Power Supply of	+1.8V (+/- .1V)
✓	Power	1.224 W (operating)
✓	UL Rating	94 V - 0

Note: The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 1.250" (31.75mm).

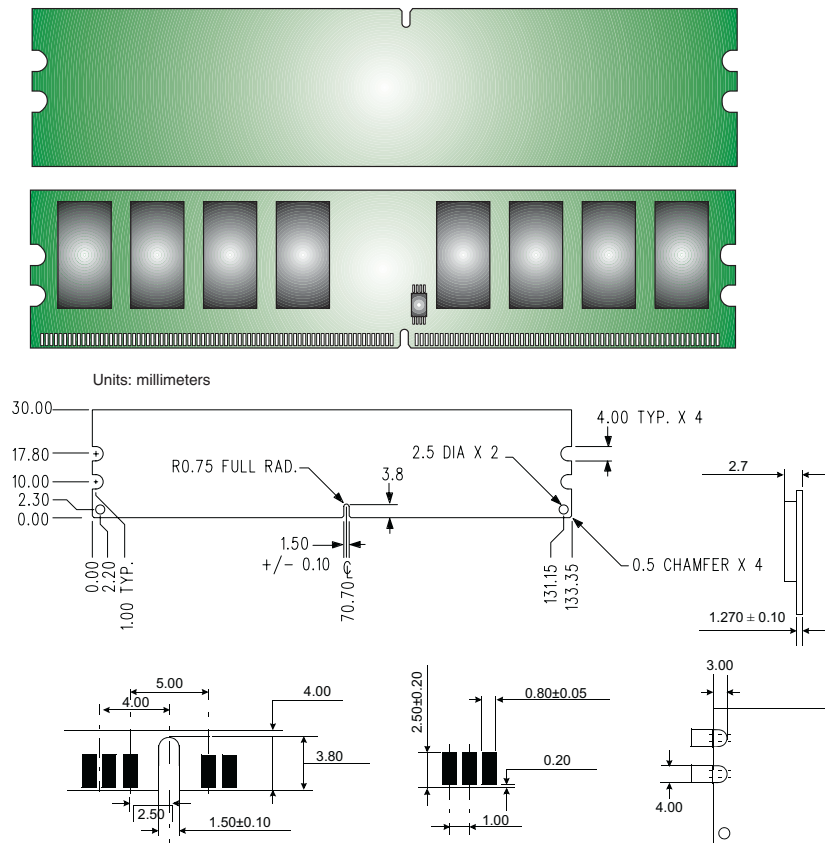


Figure 1: KVR800D2N6/512 240-Pin DIMM